3-terminal Regulator ＆ LDO Regulator Series

Standard Fixed Output LDO Regulator

Pb Free
ROHM Electronic Components

BA□□DD0,BA□□DD0W,BA□□CC0 and BA□□CC0W Series

●General Description

BA□□DD0/CC0 are low-saturation regulators, available for output s up to 2A/1A. ROHM has a wide output voltage range and package lineup with and without shutdown switches. This IC has a built-in over-current protection circuit that prevents the destruction of the IC due to output short circuits, a thermal shut-down circuit that protects the IC from damage due to overloading and an over-voltage protection circuit that protects the IC from surges generated in the power supply line of the IC.

●Features

1) Maximum output current : 2A (BA□□DD0), 1A(BA□□CC0)
2) ±1% highly accurate output voltage (BA□□DD0)
3) Low saturation with PNP output
4) Built-in over-current protection circuit that prevents the destruction of the IC due to output short circuits

5) Built-in thermal shutdown circuit for protecting the IC from damage due to overloading
6) Built-in over- voltage protection circuit that prevents the destruction of the IC due to power supply surges
7) TO220FP and HRP5 packaging (BA□□DD0) TO220FP and TO252 packaging(BA□□CC0)

●Applications

Used in DSP power supplies for DVD and CD players, FPDs, televisions, personal computers or any other consumer device

●Line up

■1A BA□□CC0 Series

Part Number	3.0	3.3	5.0	6.0	7.0	8.0	9.0	10	12	15	Package
BA□□CC0WT	○	○	○	–	○	○	○	○	○	–	TO220FP-5
BA□□CC0WT-V5	–	○	○	–	–	○	○	–	○	–	TO220FP-5（V5）
BA□□CC0WFP	–	○	○	○	○	○	○	–	○	–	TO252-5
BA□□CC0T	○	○	○	–	○	○	○	○	○	○	TO220FP-3
BA□□CC0FP	○	○	○	○	○	○	○	○	○	○	TO252-3

■2A BA□□DD0 Series

Part Number	1.5	1.8	2.5	3.0	3.3	5.0	9.0	12	16	Package
BA□□DD0WT	○	○	○	○	○	○	○	○	○	TO220FP-5
BA□□DD0WHFP	○	○	○	○	○	○	○	○	○	HRP5
BA□□DD0T	○	○	○	○	○	○	○	○	○	TO220FP-3

Part Number：BA□□CC0□□
          a   b c

Symbol	Details			
	Output Voltage Designation			
	□□	Output Voltage(V)	□□	Output Voltage(V)
a	03	3.0V(Typ.)	08	8.0V(Typ.)
	033	3.3V(Typ.)	09	9.0V(Typ.)
	05	5.0V(Typ.)	J0	10.0V(Typ.)
	06	6.0V(Typ.)	J2	12.0V(Typ.)
	07	7.0V(Typ.)	J5	15.0V(Typ.)
b	Switch:"With W":Shutdown switch included "Without W":Shutdown switch not included			
c	Package T:TO220FP-5（V5）、TO220FP-3 FP:TO252-5、TO252-3			

Part Number：BA□□DD0□□
          a   b c

Symbol	Details			
	Output Voltage Designation			
	□□	Output Voltage(V)	□□	Output Voltage(V)
a	15	1.5V(Typ.)	50	5.0V(Typ.)
	18	1.8V(Typ.)	90	9.0V(Typ.)
	25	2.5V(Typ.)	J2	12.0V(Typ.)
	30	3.0V(Typ.)	J6	16.0V(Typ.)
	33	3.3V(Typ.)		
b	Switch:"With W":Shutdown switch included "Without W":Shutdown switch not included			
c	Package T:TO220FP-5、TO220FP-3 HFP:HRP5			

Oct.2007

● Absolute Maximum Ratings(Ta=25℃)

Parameter	Symbol	Limits	Unit
Input Power Supply Voltage *1	Vcc	-0.3～+35	V
Power Dissipation *2	Pd	2300 (HRP5)	mW
		1300 (TO252-5)	
		1200 (TO252-3)	
		2000 (TO220FP-3,5)	
Operating Temperature Range	Topr	-40～+125	℃
Ambient Storage Temperature	Tstg	-55～+150	℃
Junction Temperature	Tjmax	+150	℃
Output Control Terminal Voltage *3	VCTL	-0.3～+Vcc	V
Voltage Applied to the Tip *4	Vcc peak	+50	V

*1 Must not exceed Pd
*2 HRP5 : In cases in which Ta≧25℃ when a 70mm×70mm×1.6mm glass epoxy board is used, the power is reduced by 18.4 mW/℃.
    TO252FP-3 : In cases in which Ta≧25℃ when a 70mm×70mm×1.6mm glass epoxy board is used, the power is reduced by 9.6 mW/℃.
    TO252FP-5 : In cases in which Ta≧25℃ when a 70mm×70mm×1.6mm glass epoxy board is used, the power is reduced by 10.4 mW/℃.
    TO220FP-5 : No heat sink. When Ta≧25℃, the power is reduced by 16 mW/℃.
*3 Only for models with shutdown switches.
*4 Applied voltage : 200msec or less (tr≧1msec)
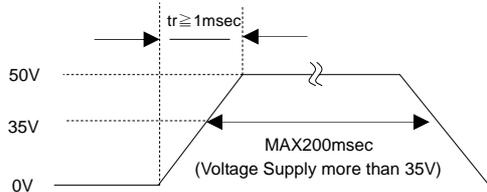
● Recommended Operating Range (Ta=25℃)

Parameter		Symbol	Min.	Max.	Unit
Input Power Supply Voltage	BA□□CC0	Vcc	4.0	25.0	V
	BA□□DD0		3.0	25.0	
Output Current	BA□□CC0	Io	—	1	A
	BA□□DD0		—	2	
Output Control Terminal Voltage		VCTL	0	Vcc	V

● Electrical Characteristics(ABRIDGED)

BA□□CC0 Series (unless specified otherwise, Ta=25℃, VCTL=5.0V(only with switch), Io=500mA,and Vcc=VccD*5)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Output Voltage	Vo	Vo×0.98	Vo	Vo×1.02	V	Refer to the lineup for Vo
Circuit Current at Shutdown	Isd	—	0	10	μA	VCTL=0V
Minimum I/O Difference	Vd	—	0.3	0.5	V	Vcc= 0.95×Vo
Output Current Capacity	Io	1.0	—	—	A	
Input Stability	Reg.I	—	20	100	mV	Vcc= (Vo+1)V → 25V
Load Stability	Reg.L	—	50	100	mV	Io=5mA→1A
Output Voltage Temperature Coefficient*6	TCVO	—	±0.02	—	%/℃	Io=5mA ,Tj=0~125℃

BA00DD0□□ series (unless specified otherwise, Ta=25℃, VCTL=3V(only with switch), Io=500mA,and Vcc=VccD*7)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Output Voltage	Vo	Vo×0.99	Vo	Vo×1.01	V	Io=200mA
Circuit Current at Shutdown	Isd	—	0	10	μA	VCTL=0V
Minimum I/O Difference	Vd	—	0.45	0.7	V	Vcc= 0.95×Vo, Io=2A
Output Current Capacity	Io	2.0	—	—	A	
Input Stability	Reg.I	—	15	50	mV	Vcc= VccD*7→25 V,Io=200mA
Load Stability	Reg.L	—	50	200	mV	Io=5mA→2A
Output Voltage Temperature Coefficient*6	TCVO	—	±0.02	—	%/℃	Io=5mA ,Tj=0~125℃

*5   Vo=3.0V : Vcc= 8.0V , Vo=3.3V : Vcc=8.3V, Vo=5.0V : Vcc=10.0V, Vo=6.0V : Vcc=11.0V, Vo=7.0V : Vcc=12.0V,
      Vo=8.0V : Vcc= 13.0V , Vo=9.0V : Vcc=14.0V, Vo=10.0V : Vcc=15.0V, Vo=12.0V : Vcc=17.0V, Vo=15.0V : Vcc=20.0V
*6   Design guarantee(100% shipping inspection not performed)
*7   Vo=1.5V , 1.8V , 2.5V , 3.0V : Vcc=4.0V, Vo=3.3V , 5.0V : Vcc=7.0V, Vo=9.0V : Vcc=12.0V
      Vo=12V : Vcc=14V , Vo=16V : Vcc=18V

●Reference Data

BA☐☐CC0☐☐(BA33CC0WT)

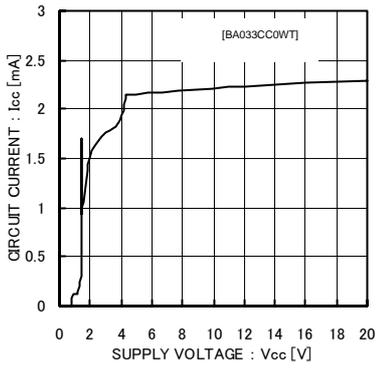(Unless specified otherwise, Vcc=8.3V, Vo=3.3V, VCTL=5.0V, and Io=0mA)

Fig.1 Circuit current

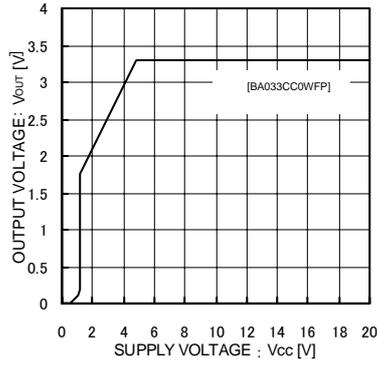X-axis: SUPPLY VOTAGE : Vcc [V] — Y-axis: CIRCUIT CURRENT : Icc [mA]
[BA033CC0WT]

Fig.2 Input Stability

X-axis: SUPPLY VOLTAGE : Vcc [V] — Y-axis: OUTPUT VOLTAGE: Vout [V]
[BA033CC0WFP]
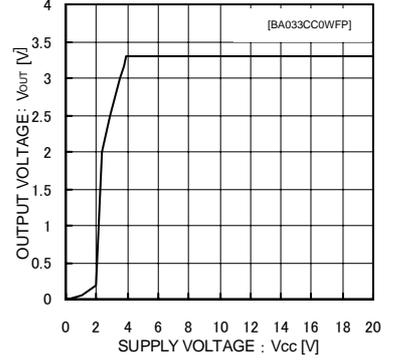
Fig.3 Input Stability
(Io=500mA)

X-axis: SUPPLY VOLTAGE : Vcc [V] — Y-axis: OUTPUT VOLTAGE: Vout [V]
[BA033CC0WFP]

Fig.4 Load Stability

X-axis: OUTPUT CURRENT : Io[mA] — Y-axis: OUTPUT VOLTAGE : Vout [V]
[BA033CC0WT]
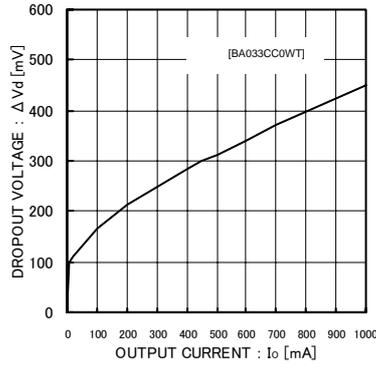
Fig.5 Input/Output Voltage Difference
Iout (0V=1A)

X-axis: OUTPUT CURRENT : Io [mA] — Y-axis: DROPOUT VOLTAGE : ΔVd [mV]
[BA033CC0WT]
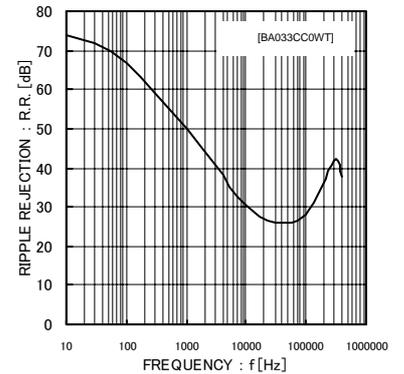
Fig.6 Ripple Rejection Characteristics
(Io=100mA)

X-axis: FREQUENCY : f [Hz] — Y-axis: RIPPLE REJECTION : R.R. [dB]
[BA033CC0WT]

Fig.7 Output Voltage Temperature Characteristics

X-axis: AMBIENT TEMPERATURE : Ta [℃] — Y-axis: OUTPUT VOLTAGE : Vout [V]
[BA033CC0WT]
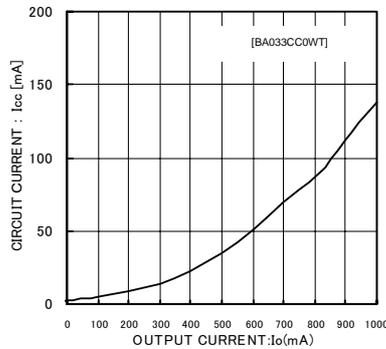
Fig.8 Circuit Current by load Level
(Iout=0mA→1A)

X-axis: OUTPUT CURRENT:Io(mA) — Y-axis: CIRCUIT CURRENT : Icc [mA]
[BA033CC0WT]

Fig.9 CTL Voltage vs. CTL Current

X-axis: CONTROL VOLTAGE:Vctl(V) — Y-axis: CIRCUIT CURRENT : Ictl [μA]
[BA033CC0WT]

Fig.10 CTL Voltage vs. Output Voltage

X-axis: CONTROL VOLTAGE : Vctl [V] — Y-axis: OUTPUT VOLTAGE : Vout [V]
[BA033CC0WFP]
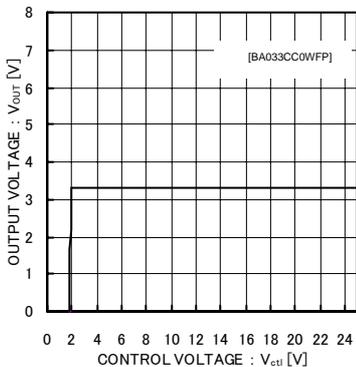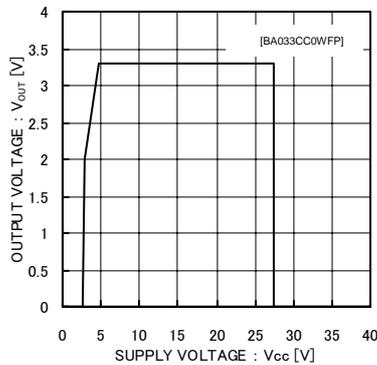
Fig.11 Overvoltage Operating Characteristics(Io=200mA)

X-axis: SUPPLY VOLTAGE : Vcc [V] — Y-axis: OUTPUT VOLTAGE : Vout [V]
[BA033CC0WFP]
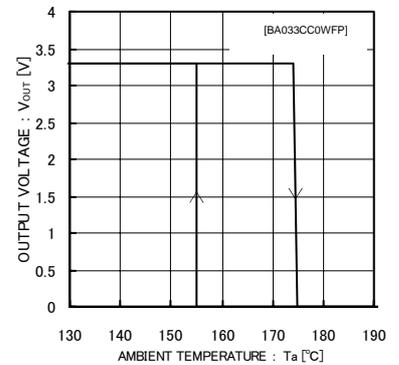
Fig.12 Thermal Shutdown Circuit Characteristics

X-axis: AMBIENT TEMPERATURE : Ta [℃] — Y-axis: OUTPUT VOLTAGE : Vout [V]
[BA033CC0WFP]

●Reference Data

BA□□DD0□□(BA50DD0WT)

(Unless specified otherwise, Vcc=7.0V, Vo=5.0V, VCTL=3.0V, and Io=0mA)

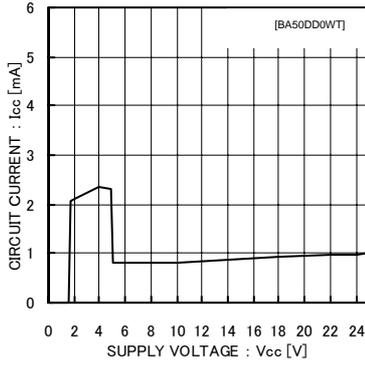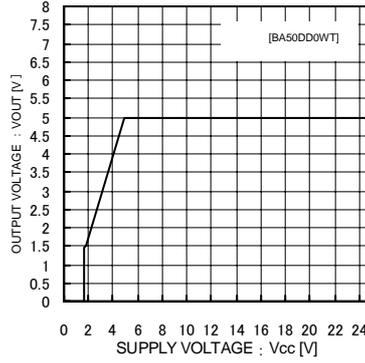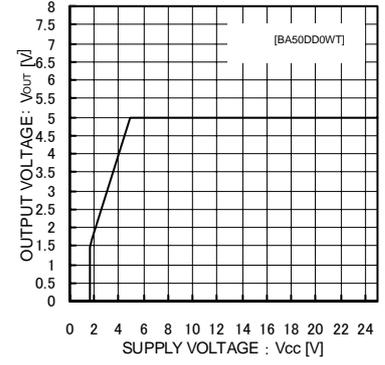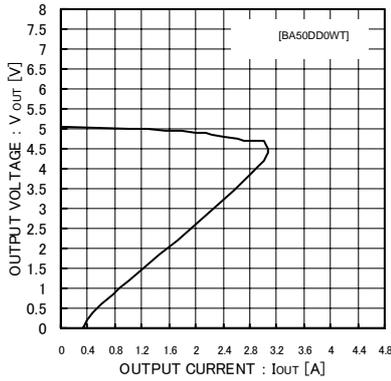Fig.13 Circuit Current

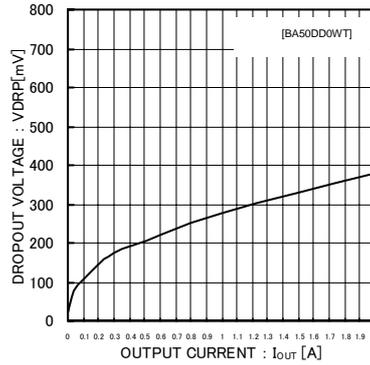Fig.14 Input Stability

Fig.15 Input Stability

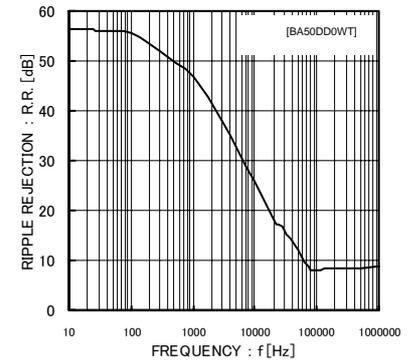Fig.16 Load Stability

Fig.17 Input/Output Voltage Difference
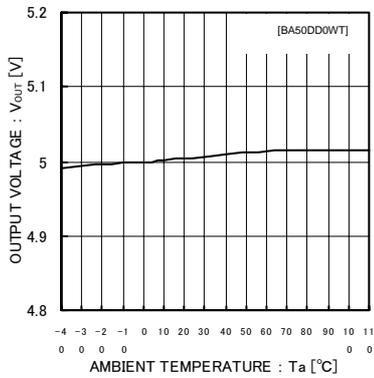（Vcc=4.75V）

Fig.18 Ripple Rejection Characteristics
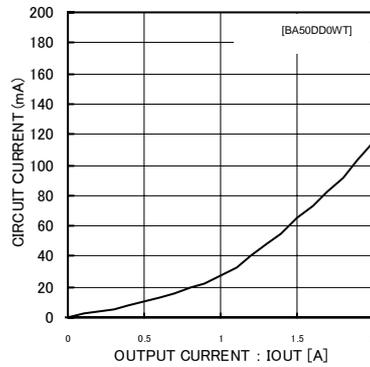（Iout=100mA）

Fig.19 Output Voltage
Temperature Characteristics
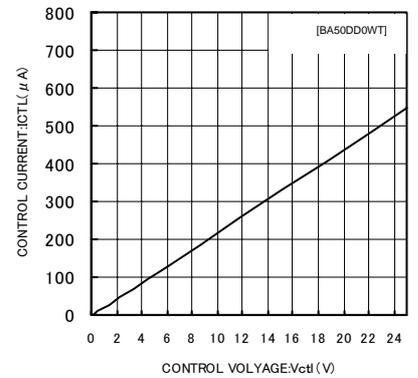
Fig.20 Circuit Current by Load Level
(IOUT=0mA→2A)
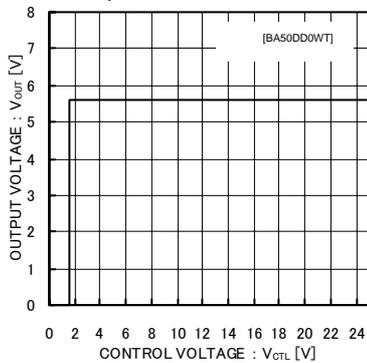
Fig.21 CTL Voltage vs. CTL Current
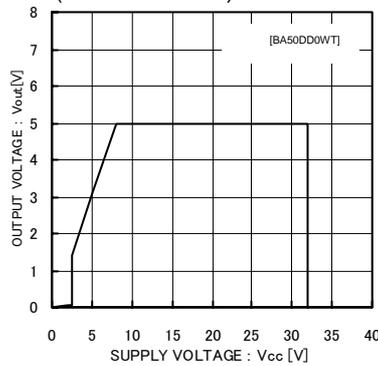
Fig.22 CTL Voltage vs. Output Voltage

Fig.23 Overvoltage Operating
（Io=200mA）

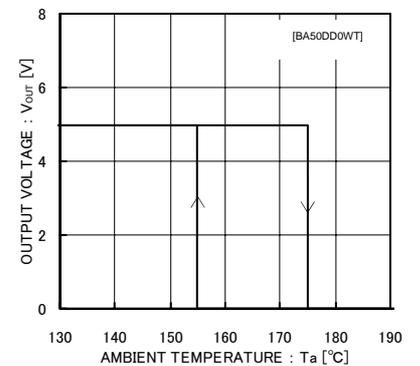Fig.24 Thermal Shutdown

●Block Diagrams

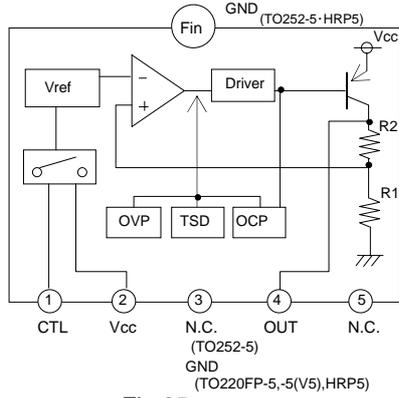### BA□□CC0WFP/ BA□□DD0WHFP/ BA□□CC0WT(V5)/ BA□□DD0WT

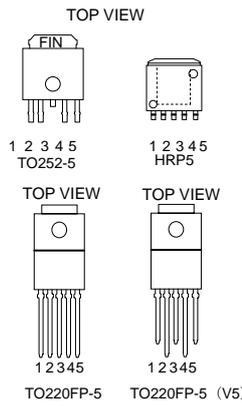Fig.25

PIN No.	Pin Name	Function	
1	CTL	Output voltage ON/OFF control	
2	Vcc	Power supply voltage input	
3	N.C/GND	Unconnected terminal/GND	*1
4	OUT	Voltage output	
5	N.C	Unconnected terminal	
Fin	GND	GND	*2

*1 TO252-5 is N.C.,and TO220FP-5,-5(V5),and HRP5 are GND
*2 Only for TO252-5 and HRP5

BA□□CC0T/ BA□□CC0FP/ BA□□DD0T

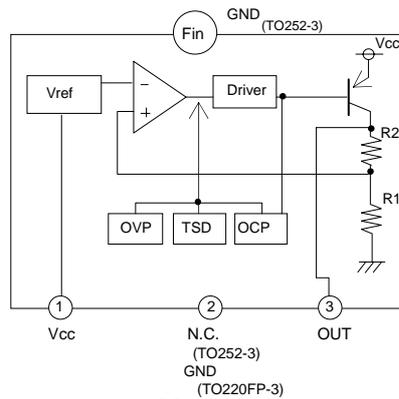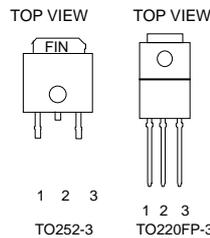Fig.26

PIN No.	Pin Name	Function	
1	Vcc	Power supply voltage input	
2	N.C/GND	Unconnected terminal/GND	*1
3	OUT	Voltage output	
Fin	GND	GND	*2

*1 TO252-3 is N.C.,and TO-220FP-3,is GND
*2 Only for TO252-3 and HRP5
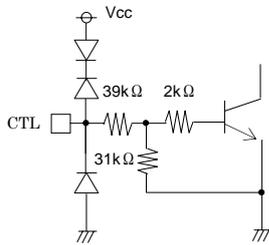
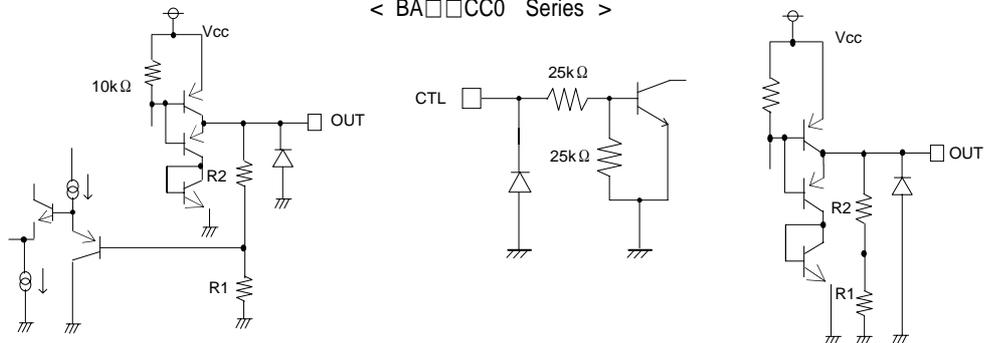## ●Input / Output Equivalent Circuit Diagrams

< BA□□DD0 Series >

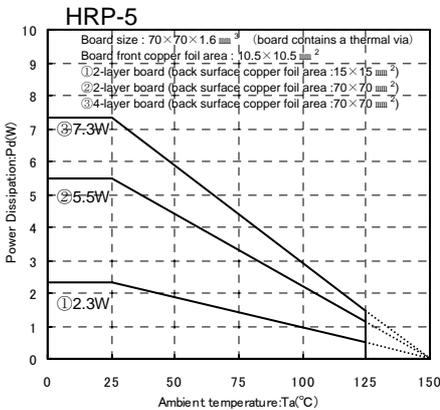

Fig.27

< BA□□CC0 Series >

Fig.28
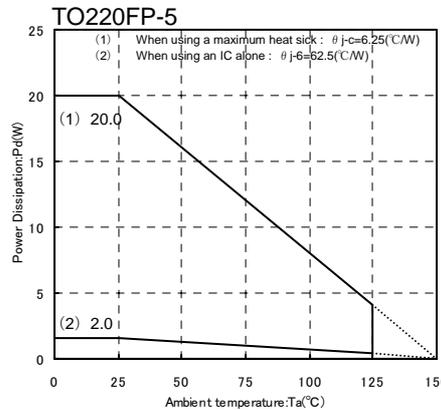
●Thermal Design

HRP-5

Fig.29

TO220FP-5

Fig.30

TO252-5
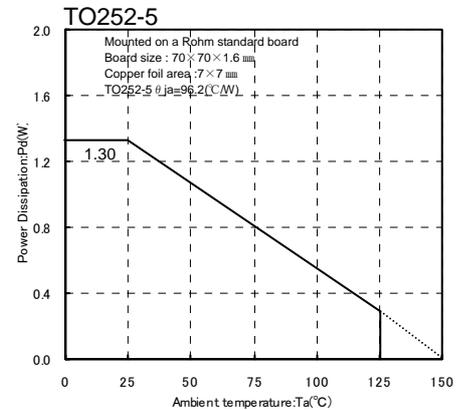
Fig.31

When using at temperatures over Ta=25℃, please refer to the heat reducing characteristics shown in Fig.29 through 31. The IC characteristics are closely related to the temperature at which the IC is used and if the temperature exceeds the maximum junction temperature TjMAX., the elements may be damaged or destroyed. From the standpoints of instantaneous destruction and long-term operating reliability, it is necessary give sufficient consideration to IC heat. In order to protect the IC from thermal damage, it is necessary to operate it at temperatures lower than the maximum junction temperature TjMAX of the IC.

Fig.30 shows the acceptable loss and heat reducing characteristics of the TO220FP package The portion shown by the diagonal line is the acceptable loss range that can be used with the IC alone. Even when the ambient temperature Ta is a normal temperature (25℃), the chip (junction) temperature Tj may be quite high so please operate the IC at temperatures less than the acceptable loss Pd.

The method of calculating the power consumption Pc(W) is as follows.

Vcc : Input voltage
Vo : Output voltage
Io : Load current
Icca : Circuit current

$$Pc = (Vcc-Vo) \times Io + Vcc \times Icca$$
Acceptable loss $Pd \leqq Pc$

Solving this for load current IO in order to operate within the acceptable loss:

$$Io \leqq \frac{Pd - Vcc \times Icca}{Vcc - Vo}$$

(Please refer to Figs.8 and 20 for Icca.)

It is then possible to find the maximum load current Io_{MAX} with respect to the applied voltage Vcc at the time of thermal design.

・Calculation Example
　Example 1) When Ta=85℃, Vcc=8.3V, Vo=3.3V, BA33DD0WT

$$Io \leqq \frac{1.04 - 8.3 \times Icca}{5}$$
$Io \leqq 200mA$ (Icca : 2mA)

With the IC alone : θja=62.5℃/W → -16mW/℃
25℃=2000mW → 85℃=1040mW

　　Please refer to the above information and keep thermal designs within the scope of acceptable loss for all operating temperature ranges.
　　The power consumption Pc of the IC when there is a short circuit (short between Vo and GND) is :
　　　$Pc = Vcc \times (Icca + Ishort)$

＊Ishort : Short circuit current

●Peripheral Circuit Considerations
　・Vcc Terminal
　　Please attach a capacitor (greater than 0.33μF) between the Vcc and GND.
　　The capacitance values will differ depending on the application, so please take this into account when configuring the terminal.
　・GND Terminal
　　Please be sure to keep the set ground and IC ground at the same potential level so that a potential difference does not arise between them.
　　If a potential difference arises between the set ground and the IC ground, the preset voltage will not be outputted, causing the system to become unstable. Therefore, please reduce the impedance by making the ground patterns as wide as possible and by reducing the distance between the set ground and the IC ground as much as possible.
　・CTL Terminal
　　The CTL terminal is turned ON at 2.0V and higher and OFF at 0.8V and lower within the operating power supply voltage range.
　　The power supply and the CTL terminal may be started up and shut down in any order without problems.
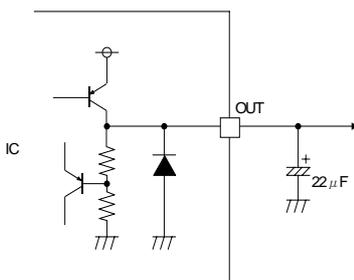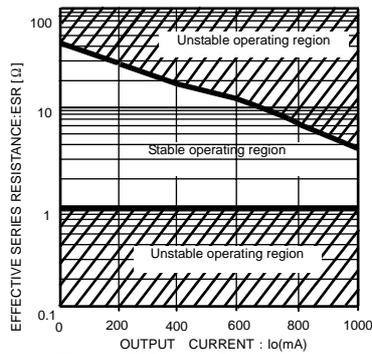
●Vo Terminal

Fig.32 Output Equivalent Circuit
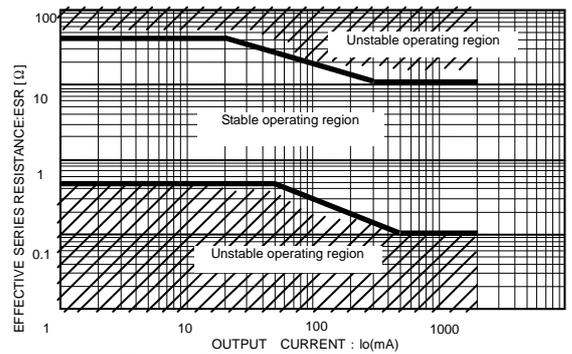

Fig.33 ESR-Io Characteristics
(BA□□CC0,22μF)

Fig.34 ESR vs Io Characteristics
(BA□□DD0,22μF)

　Please attach an anti-oscillation capacitor between Vcc and GND. The capacitance of the capacitor may significantly change due to factors such as temperature changes, making it impossible to completely stop oscillations. Please use a tantalum capacitor or aluminum electrolysis capacitor with favorable characteristics and small internal series resistance (ESR) even at low temperatures. The output fluctuates regardless of whether the ESR is large or small. Please use the IC within the stable operating region while referring to the ESR characteristics reference data shown in Figs.32 through 34. In applications where there are sudden load fluctuations, the use of a capacitor with large capacitance is recommended.
　Below figure , it is ESR-to-Io stability Area characteristics ,measured by 22μF -ceramic-capacitor and resistor connected in series.
　This characteristics is not equal value perfectly to 22μF -aluminum electrolytic capacitor in order to measurement method.
　Note, however, that the stable range suggested in the figure depends on the IC and the resistance load involved, and can vary with the board's wiring impedance, input impedance, and/or load impedance. Therefore, be certain to ascertain the final status of these items for actual use.
　Keep capacitor capacitance within a range of 22μF～1000μF. It is also recommended that a 0.33μF bypass capacitor be connected as close to the input pin-GND as location possible. However, in situations such as rapid fluctuation of the input voltage or the load, please check the operation in real application to determine proper capacitance.

● Other Points of Caution

1)Protection Circuits

Over-current Protection Circuit

A built-in over-current protection circuit corresponding to the current capacity prevents the destruction of the IC when there are load shorts. This protection circuit is a "7"-shaped current control circuit that is designed such that the current is restricted and does not latch even when a large current momentarily flows through the system with a high-capacitance capacitor. However, while this protection circuit is effective for the prevention of destruction due to unexpected accidents, it is not suitable for continuous operation or transient use. Please be aware when creating thermal designs that the overcurrent protection circuit has negative current capacity characteristics with regard to temperature (Refer to Figs.4 and 16).

Thermal Shutdown Circuit (Thermal Protection)

This system has a built-in temperature protection circuit for the purpose of protecting the IC from thermal damage. As shown above, this must be used within the range of acceptable loss, but if the acceptable loss happens to be continuously exceeded, the chip temperature Tj increases, causing the temperature protection circuit to operate.

When the thermal shutdown circuit operates, the operation of the circuit is suspended. The circuit resumes operation immediately after the chip temperature Tj decreases, so the output repeats the ON and OFF states (Please refer to Figs.12 and 24 for the temperatures at which the temperature protection circuit operates).

There are cases in which the IC is destroyed due to thermal runaway when it is left in the overloaded state. Be sure to avoid leaving the IC in the overloaded state.

Reverse Current

In order to prevent the destruction of the IC when a reverse current flows through the IC, it is recommended that a diode be placed between the Vcc and Vo and a pathway be created so that the current can escape (Refer to Fig.35).

2) This IC is bipolar IC that has a P-board (substrate) and P+ isolation layer between each devise, as shown in Fig.36. A P-N junction is formed between this P-layer and the N-layer of each device, and the P-N junction operates as a parasitic diode when the electric potential relationship is GND> Terminal A, GND> Terminal B, while it operates as a parasitic transistor when the electric potential relationship is Terminal B GND> Terminal A. Parasitic devices are structurally inevitable in the IC. The operation of parasitic devices induces mutual interference between circuits, causing malfunctions and eventually the destruction of the IC. It is necessary to be careful not to use the IC in ways that would cause parasitic elements to operate. For example, applying a voltage that is lower than the GND (P-board) to the input terminal.
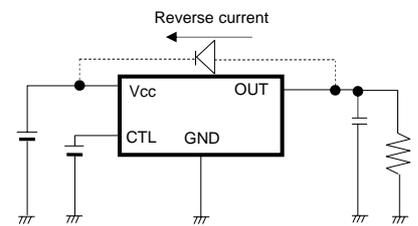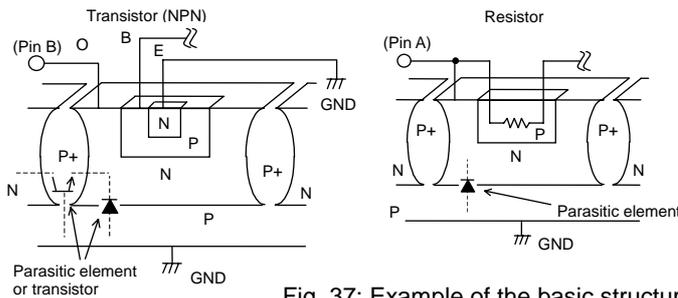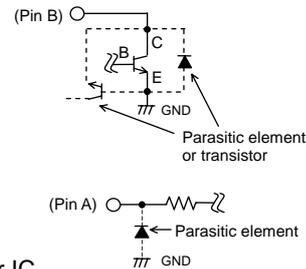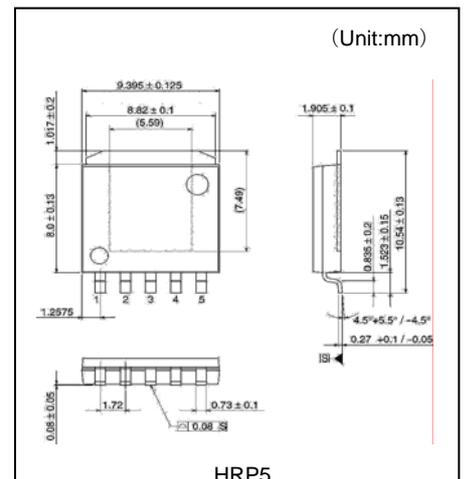
Fig. 36:Bypass diode

Fig. 37: Example of the basic structure of a bipolar IC

●Part Number Selection

| B | A | 3 | 3 | D | D | 0 | W | H | F | P | – | E | 2 |

ROHM model name

Output voltage

Current capacity
CC0 : 1A
DD0 : 2A

Shutdown switch
W : With switch
None : Without switch

Package
T : TO220-3,5
F P : TO252-3,5
HFP : HRP5

Package specification
TR : Embossed taping(HRP5)
E2 : Embossed taping(TO252-3,5)
None : Tube container
V5 :Foaming(V5 only)

TO252-3

TO252-5

HRP5

<table>
<tr><td colspan="2">＜Package Specification＞HRP5</td></tr>
<tr><td>Package   Form</td><td>Embossed taping</td></tr>
<tr><td>Package Quantity</td><td>2000pcs</td></tr>
<tr><td>Package Orientation</td><td>TR
（When the reek is held with the left hand and the tape is drawn out with the right hand, the No.1 pin of the product faces the upper right direction.）</td></tr>
</table>

Reel   No.1 pin   Pulling side →
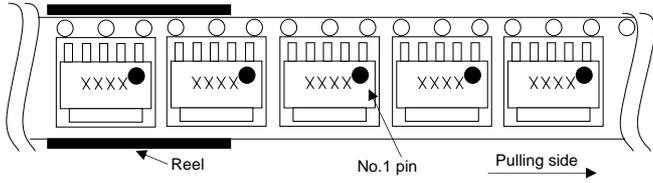
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<tr><td colspan="2">< Package Specification > TO252-3,5</td></tr>
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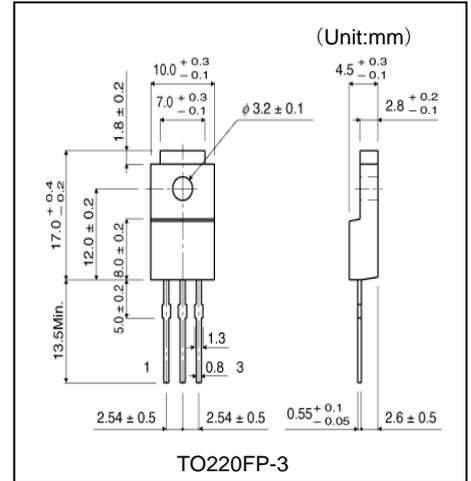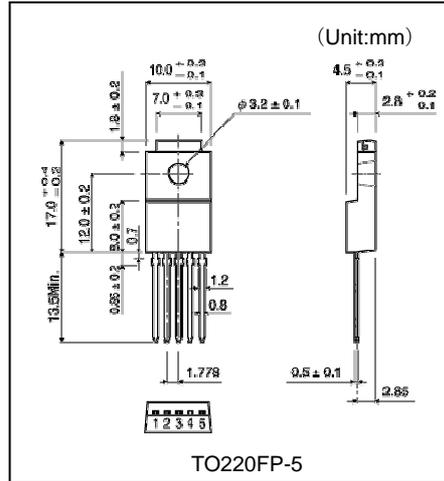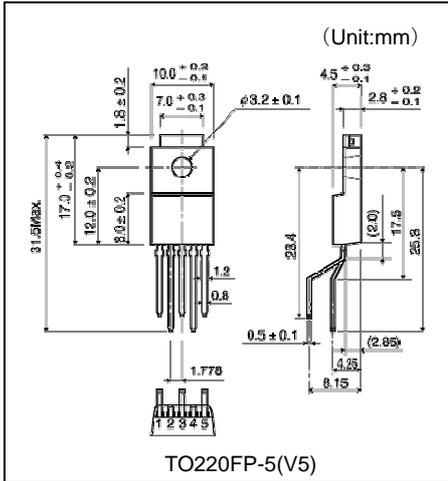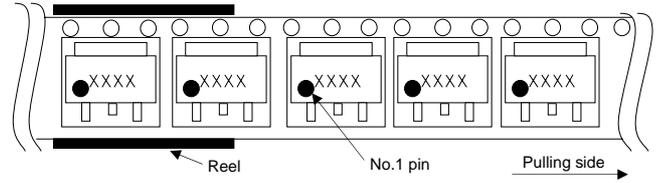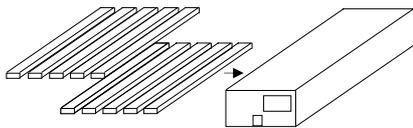<tr><td>Package Quantity</td><td>2000pcs</td></tr>
<tr><td>Package Orientation</td><td>E2
（When the reek is held with the left hand and the tape is drawn out with the right hand, the No.1 pin of the product faces the lower left direction.）</td></tr>
</table>

Reel   No.1 pin   Pulling side →

（Unit:mm）

TO220FP-5(V5)

（Unit:mm）

TO220FP-5

（Unit:mm）

TO220FP-3

<table>
<tr><td colspan="2">＜Package Specification＞TO220FP-5 （V5）</td></tr>
<tr><td>Package Form</td><td>Container tube</td></tr>
<tr><td>Package Quantity</td><td>500pcs</td></tr>
<tr><td>Package Orientation</td><td>The product orientation in each container tube is constant.</td></tr>
</table>

*Please make orders in multiples of the package quantity.

<table>
<tr><td colspan="2">＜Package Specification＞TO220FP-5</td></tr>
<tr><td>Package Form</td><td>Container tube</td></tr>
<tr><td>Package Quantity</td><td>500pcs</td></tr>
<tr><td>Package Orientation</td><td>The product orientation in each container tube is constant.</td></tr>
</table>

*Please make orders in multiples of the package quantity.

<table>
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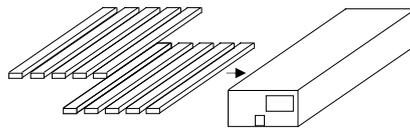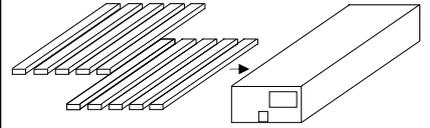<tr><td>Package Form</td><td>Container tube</td></tr>
<tr><td>Package Quantity</td><td>500pcs</td></tr>
<tr><td>Package Orientation</td><td>The product orientation in each container tube is constant.</td></tr>
</table>

*Please make orders in multiples of the package quantity.

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